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PATENT, TRADEMARK, COPYRIGHT
AND UNFAIR COMPETITION LAW
AND RELATED LITIGATION

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July 13, 2005

FACSIMILE COVER SHEET

To: Examiner Phat X. Cao
Group Unit No. 2814
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Fax: 703-872-9306

From: William R. Allen, Ph.D.

Re: US Application Serial No. 10/767,039
Filed January 29, 2004
Title: VERTICAL FIELD EFFECT
TRANSISTORS INCORPORATING
SEMICONDUCTING NANOTUBES
GROWN IN A SPACER-DEFINED
PASSAGE
Applicants: Toshiharu Furukawa et al.
Atty Docket: IBM-273

Pages: 8 (including cover sheet)

MESSAGE/COMMENTS

Please see the attached Response to Restriction
Requirement and Preliminary Amendment for filing in
connection with the above-referenced application.

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William R. Allen 13 July 2005
William R. Allen Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Toshiharu Furukawa, et al. Art Unit: 2814
Serial No.: 10/767,039 Examiner: Phat X. Cao
Filed: January 29, 2004 Atty. Docket No.: ROC920030272US1
For: VERTICAL FIELD EFFECT TRANSISTORS INCORPORATING
SEMICONDUCTING NANOTUBES GROWN IN A SPACER-DEFINED
PASSAGE

**RESPONSE TO RESTRICTION REQUIREMENT AND
PRELIMINARY AMENDMENT**

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action of June 24, 2005, please amend the above-identified application as follows:

Amendments to the claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks begin on page 6 of this paper.

Page 1 of 7
Serial No. 10/703,355
Response to Restriction Requirement dated July 13, 2005
Submitted in Reply to Restriction of June 24, 2005
IBM Docket ROC920030272US1
WH&E IBM/273
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